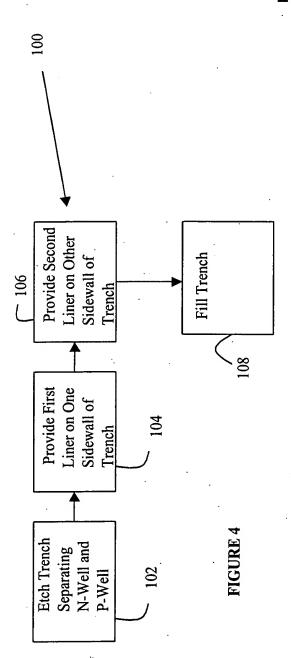


## Title: SHALLOW TRENCH ISOLATION FOR STRAINED SILICON PROCESSES Inventor(s): Srinath Krishnan DOCKET NO.: 039153-0694

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